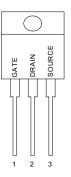


GENERAL DESCRIPTION

This Power MOSFET is designed for low voltage, high speed power switching applications such as switching regulators, conveters, solenoid and relay drivers.

PIN CONFIGURATION

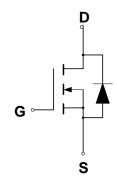
TO-220/TO-220FP Top View



FEATURES

- Higher Current Rating
- Lower r_{DS(ON)}, Lower Capacitances
- Lower Total Gate Charge
- Tighter VSD Specifications
- Avalanche Energy Specified

SYMBOL



N-Channel MOSFET

ORDERING INFORMATION

Part Number	Package
CMT05N50N220	TO-220
CMT05N50N220FP	TO-220FP

ABSOLUTE MAXIMUM RATINGS

Rating		Value	Unit
Drain to Current – Continuous		5.0	А
 Pulsed (Note 1) 	I _{DM}	18	
Gate-to-Source Voltage — Continue	V_{GS}	±20	V
Total Power Dissipation	PD	96	W
Derate above 25°C		0.77	W/°C
Single Pulse Avalanche Energy (Note 2)	E _{AS}	125	mJ
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C
Thermal Resistance – Junction to Case		1.70	°C/W
 Junction to Ambient 	θ_{JA}	62	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		300	°C



ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_{\rm J}$ = 25 $^\circ\!{\rm C}$.

			CMT05N50		1	Units
Characteristic		Symbol	Min	Тур	Max	
Drain-Source Breakdown Voltage		V _{(BR)DSS}	500			V
(V _{GS} = 0 V, I _D = 250 µ A)						
Drain-Source Leakage Current		I _{DSS}				μA
$(V_{DS} = 500V, V_{GS} = 0 V)$					25	
Gate-Source Leakage Current-Forward		I _{GSSF}			100	nA
$(V_{gsf} = 20 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Reverse		I _{GSSR}			-100	nA
$(V_{gsr} = -20 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		V _{GS(th)}	2.0		4.0	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistance		R _{DS(on)}			1.5	Ω
Forward Transconductance (V_{DS} = 15V, I_D = 2.5 A) (Note 4)		g fs	2.8			mhos
Input Capacitance	(V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz)	C _{iss}		520	730	pF
Output Capacitance		C _{oss}		170	240	pF
Reverse Transfer Capacitance		C _{rss}		11	20	pF
Turn-On Delay Time		t _{d(on)}		7.0	10	ns
Rise Time	(V _{DD} = 250 V, I _D = 5 A,	tr		9.0	20	ns
Turn-Off Delay Time	R_{G} = 9.1 Ω , V_{GS} = 10 V) (Note 4)	t _{d(off)}		20	40	ns
Fall Time		t _f		10	20	ns
Total Gate Charge	(V _{DS} = 400V, I _D = 5A	Qg		10		nC
Gate-Source Charge	$V_{GS} = 400V, I_D = 5A$ $V_{GS} = 10 V) (Note 4)$	Q_gs		2		nC
Gate-Drain Charge	$V_{GS} = 10$ V) (Note 4)	Q_gd		3		nC
Internal Drain Inductance		LD		4.5		nH
(Measured from the drain lead 0.2	5" from package to center of die)					
Internal Drain Inductance		Ls		7.5		nH
(Measured from the source lead 0	.25" from package to source bond pad)					
SOURCE-DRAIN DIODE CHARAC	TERISTICS					_
Reverse Recovery Charge		Qrr		1.8		μC
Forward Turn-On Time	I_F = 5A, di/dt = 100A/µs , T_J = 25 $^\circ\!\mathrm{C}$	t _{on}		**		
Reverse Recovery Time		t _{rr}		415		ns
Diode Forward Voltage	I _S = 5A, V _{GS} = 0 V	V _{SD}			1.5	V

Note

(1) Repetitive rating; pulse width limited by max. junction temperature

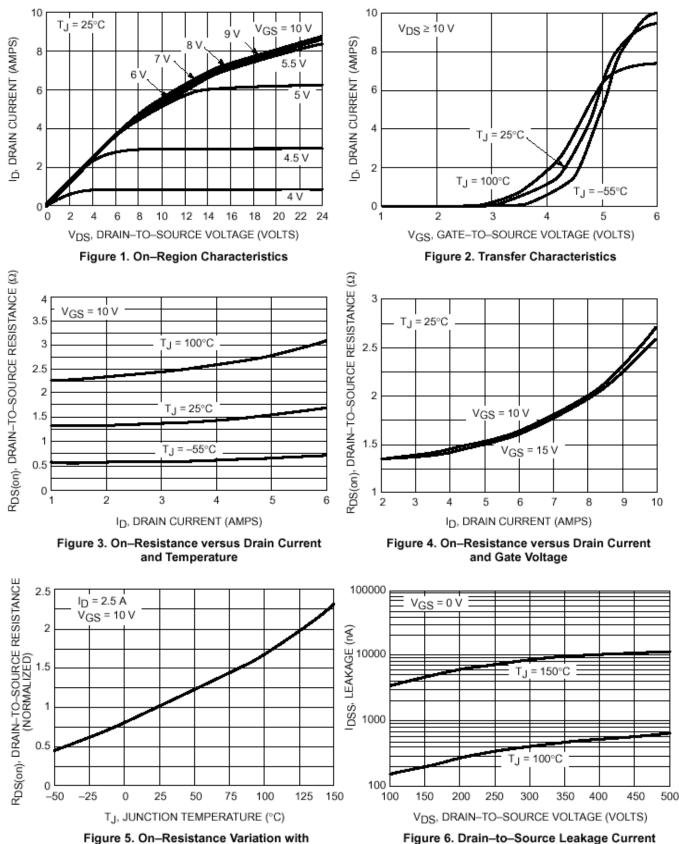
(2) V_{DD} = 100V, V_{GS} = 10V, L=10mH, I_{AS} = 5A, R_{G} = 25 Ω

(3) I_{SD}~\leq~4.5A,\,di/dt~\leq~75A/\mu s,\,V_{DD}~\leq~V_{(BR)DSS},\,T_{J}~\leq~150^{\circ}\!\mathrm{C}

** Negligible, Dominated by circuit inductance



TYPICAL ELECTRICAL CHARACTERISTICS

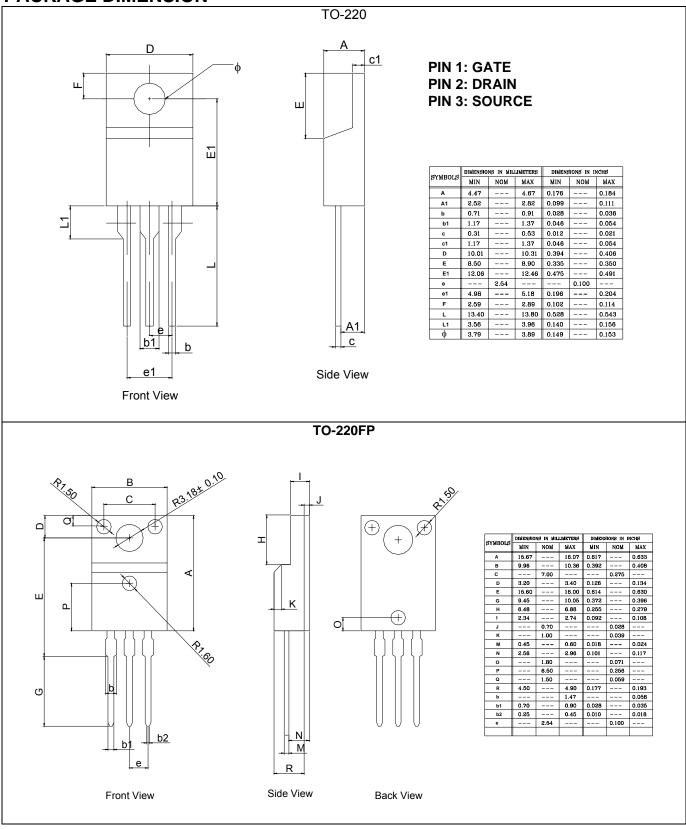


versus Voltage

Temperature



PACKAGE DIMENSION





IMPORTANT NOTICE

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HsinChu Headquarter	Sales & Marketing		
5F-1, No. 11, Park Avenue II,	7F-6, No.32, Sec. 1, Chenggong Rd., Nangang		
Science-Based Industrial Park,	District, Taipei City 115, Taiwan		
HsinChu City, Taiwan			
TEL: +886-3-567 9979	TEL: +886-2-2788 0558		
FAX: +886-3-567 9909	FAX: +886-2-2788 2985		